CURRICULUM VITAE

PERSONAL INFORMATION

Stavros Stavrinidis

Date of birth : 30/4/1988
Residence address : Athens (Greece)
E-mail : stavst@hotmail.com
Military obligations : Fulfilled

EDUCATION AND TRAINING

2018–Today

PhD Student
University of West Attica, Athens (Greece)
Department of Industrial Design and Production Engineering

2013–2015

Master's Degree (M.Sc.) in "Advanced Information Systems"
University of Piraeus, Athens (Greece)
(grade: 9.38)

2007–2013

Bachelor's Degree in "Automation Engineering"
Technological Educational Institute of Piraeus, Athens (Greece)
(grade: 7.22)

LANGUAGE SKILLS

English

1. Certificate of Competency in English (B2) – University of Michigan
2. Advanced Level Certificate in English (C1) – Hellenic American University

COMPUTER SKILLS

- Microsoft Office (Word, Excel, Power Point, Access)
- Programming languages: C# /.Net (Windows Forms, WPF), VB.NET
- DataBases : Microsoft SQL Server
- Software: Microsoft Visual Studio, Matlab /Simulink
- Additional skills:

i) Arduino (ATmega328) microcontroller programming

ii) Mobile device application development: Programming in Android (Android Studio, M.I.T AppInventor)

iii) Digital image processing: Edge detection, Texture and shape analysis (MATLAB)

iv) Digital sound and speech processing (MATLAB)

ADDITIONAL INFORMATION

Publications

1."Optimization of PID controllers using GA with GUI for educational aims", S. Stavrinidis, A. Dounis, P. Kofinas, N. Katsikogiannis in Production Control Session of International Scientific Conference eRA-8, organized by T.E.I. of Piraeus at T.E.I. Piraeus Campus, 23 – 25 September (2013).
2. "Electrochromic device modeling using an adaptive neuro-fuzzy inference system: A model-free approach", Anastasios I Dounis, G. Leftheriotis, S. Stavrinidis, G. Syrrokostas in international journal Energy and Buildings (2015).
3. "Fuzzy-PID Controller for MPPT of PV system optimized by Big Bang-Big Crunch algorithm", A.I. Dounis, S. Stavrinidis, P. Kofinas, IEEE International Conference on Fuzzy Systems, Turkey (2015).
4. "An FPGA-based Accelerated Optimization Algorithm for Real-Time Applications" , Mihalis Psarakis , Anastasios Dounis , Almabrok Abdoalnasir, Stavros Stavrinidis, Georgios Gkekas, Journal of Signal Processing Systems (2020).